19 9 - 59 58 57 56 AWAKING DEGREE PREDICTING UNIT BLINKING TIME CALCULATING UNIT OPEN/CLOSE JUDGING UNIT WARNING PRODUCING UNIT RETINA REFLECTION DETECTING UNIT RAM SYSTEM CONTROL UNIT A/D CONVER-TING UNIT 55 62 ILLUMINATION LIGHT-CAMERA CONTROL UNIT 54 53 14 14a 14b 14c 13 V 63

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FIG. 2

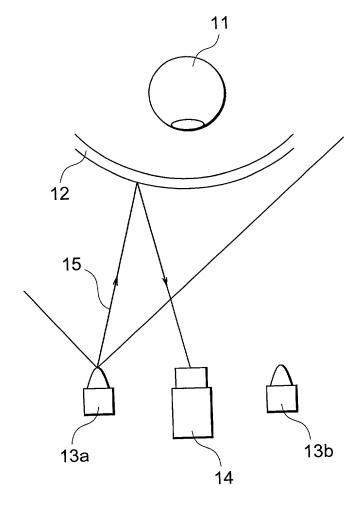


FIG. 3

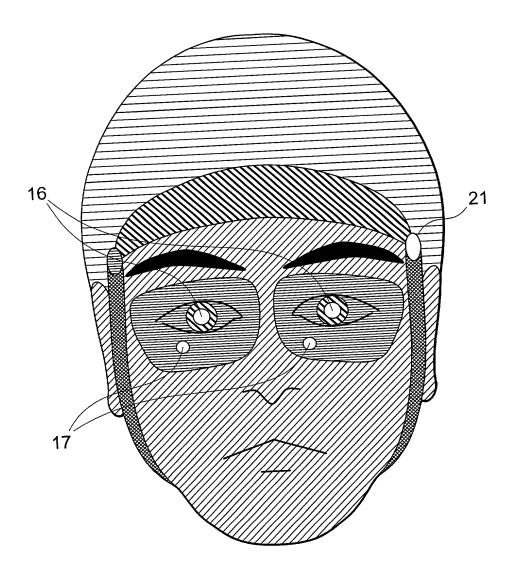


FIG. 4

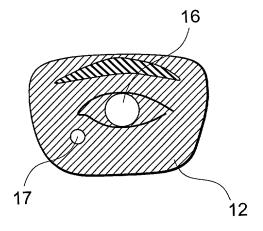


FIG. 5

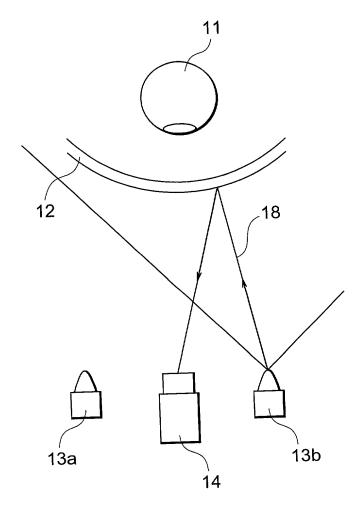


FIG. 6

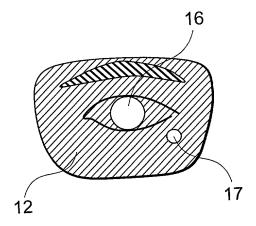


FIG. 7

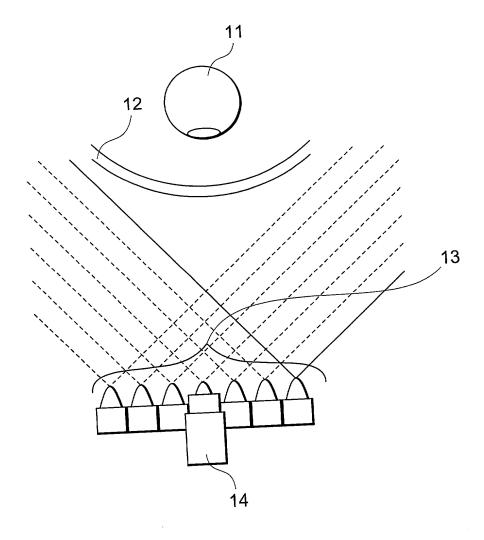


FIG. 8

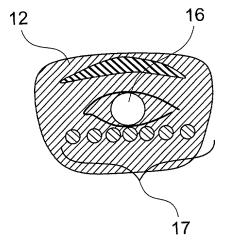


FIG. 9

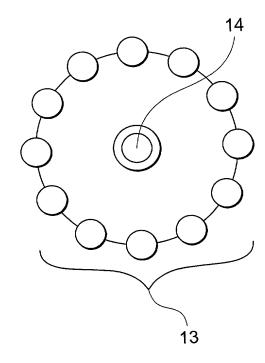


FIG. 10

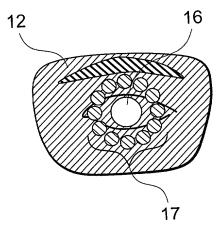


FIG. 11

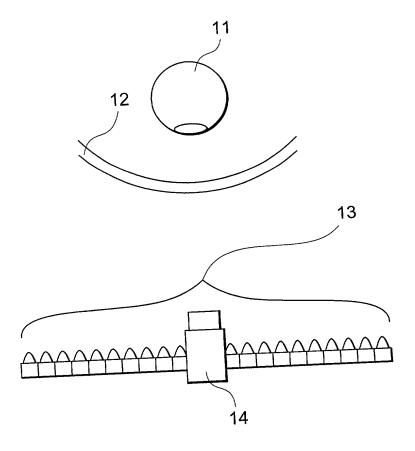
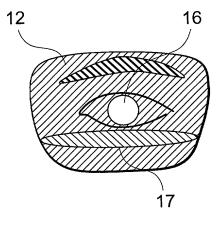
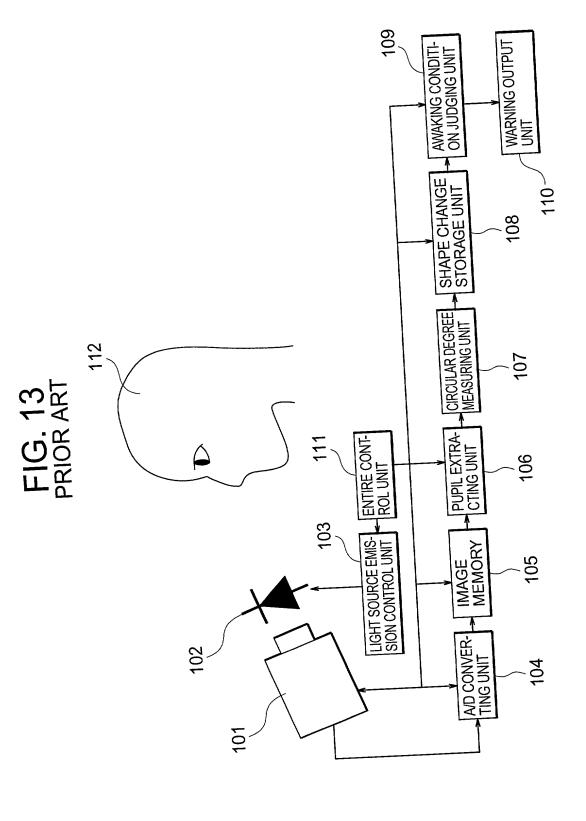


FIG. 12





OUTPUT I/F CPU **B** RAM 131 129 128 126 127 IMAGE PROCESS-ING UNIT OUTPUT I/F 125 IMAGE MEMORY INPUT I/F ROM 130 ΑD FIG. 14 PRIOR ART 121a 121b 123 J121c(122 ا> 124 121d-۵

132

133

FIG. 15 PRIOR ART

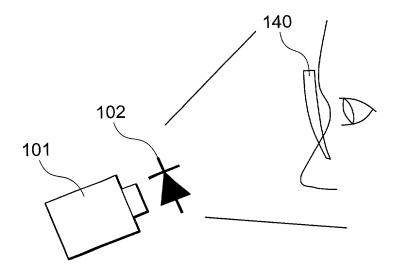


FIG. 16 PRIOR ART

